



Corres a Mal.  
**BOX AF**

AF/2652  
#85/E  
6/13/04

**UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of: **KIRA, et al.**

Serial No.: **09/548,313**

Group Art Unit: **2652**

Filed: **April 12, 2000**

Examiner: **Craig A. Renner**

P.T.O. Confirmation No.: **6169**

For: **HEAD ASSEMBLY HAVING INTEGRATED CIRCUIT CHIP COVERED BY LAYER WHICH PREVENTS FOREIGN PARTICLE GENERATION**

**RESPONSE UNDER 37 CFR §1.116**  
**- EXPEDITED RESPONSE -**  
**GROUP ART UNIT 2652**

**RECEIVED**

**JUN 14 2004**

**Technology Center 2600**

**MAIL STOP AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**June 8, 2004**

Sir:

In response to the Office Action dated **April 23, 2004**, please amend the above-identified application as follows:

**Please Enter**  
**CR/12-1704**